



# Intel® SSD 520 Series

Specification Update

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*June 2012*

*Revision 1.0*

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## Revision History

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Revision	Description	Date
-001	Initial Release	June 2012

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# Preface

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This document is an update to the specifications contained in the documents listed in the following Affected Documents/Related Documents table. It is a compilation of device and document errata and specification clarifications and changes, and is intended for hardware system manufacturers and for software developers of applications, operating system, and tools.

Information types defined in the Nomenclature section of this document are consolidated into this update document and are no longer published in other documents. This document may also contain information that has not been previously published.

## Affected Documents

Document Title	Document Number/Location
Intel® SSD 520 Series Product Specification	325968-001

## Nomenclature

**Errata** are design defects or errors. Errata may cause the Intel SSD 520 Series' behavior to deviate from published specifications. Hardware and software designed to be used with any given stepping must assume that all errata documented for that stepping are present on all devices.

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Note:** Errata remain in the specification update throughout the product's lifecycle, or until a particular stepping is no longer commercially available. Under these circumstances, errata removed from the specification update are archived and available upon request. Specification changes, specification clarifications and documentation changes are removed from the specification update when the appropriate changes are made to the appropriate product specification or user documentation (datasheets, manuals, etc.).





# Summary Tables of Changes

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The following table indicates the Specification Changes, Errata, Specification Clarifications or Documentation Changes, which apply to the listed MCH steppings. Intel intends to fix some of the errata in a future stepping of the component, and to account for the other outstanding issues through documentation or Specification Changes as noted. This table uses the following notations:

## Codes Used in Summary Table

### Status

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future stepping of the product.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.

### Row

Shaded:	This item is either new or modified from the previous version of the document.
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Number	FW Affected	Plans	ERRATA
	400i		
1	X	No Fix	AES Encryption functionality limited to AES 128-bit Encryption

Number	SPECIFICATION CHANGES
1	Specification is changed from AES 256-bit encryption to AES 128-bit encryption

Number	SPECIFICATION CLARIFICATIONS
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Number	DOCUMENTATION CHANGES
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## Errata

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### 1. AES Encryption functionality limited to AES 128-bit Encryption

**Problem:** As part of ongoing quality assurance, Intel Corporation has discovered a limitation of the AES Encryption feature in the Intel® SSD 520 Series.

**Implication:** AES encryption strength is AES 128-bit.

**Workaround:** None.

**Status:** No Fix. Specification is changed from AES 256-bit encryption to AES 128-bit encryption.

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## Specification Changes

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1. Specification is changed from AES 256-bit encryption to AES 128-bit encryption.

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## *Specification Clarifications*

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There are no specification clarifications in this Specification Update revision.

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## ***Documentation Changes***

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There are no documentation changes in this Specification Update revision.

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